

T2802 SERIES

High-reliability discrete products and engineering services since 1977

SILICON BIDIRECTIONAL THYRISTORS

FEATURES

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Repetitive peak off-stage voltage ⁽¹⁾				
(T ₁ = -40 to +100°C, gate open)				
T2802B		200		
T2802C	V _{DRM}	300	Volts	
T2802D		400		
T2802E		500		
T2802M		600		
RMS on-state current (conduction angle = 360°, T _c = 80°C)	I _{T(RMS)}	8	Amps	
Peak non-repetitive surge current (One Cycle, 60Hz, TJ = 80°C)	I _{TSM}	100	Amps	
Circuit fusing considerations	l ² t		A ² -	
(T _j = -40 to +100°C, t = 1.25 to 10ms)	It	50	A ² s	
Peak gate power (pulse width = 1.0µs)	P _{GM}	16	Watts	
Average gate power	P _{G(AV)}	0.35	Watts	
Peak gate trigger current (pulse width = 1.0µs)	I _{GM}	4	Amps	
Operating junction temperature range	Tj	-40 to +100	°C	
Storage temperature range	T _{stg}	-40 to +150	°C	

Note 1: Ratings apply for open gate conditions. Thyristor devices shall not be tested with a constant current source for blocking capability such that the voltage applied exceeds the rated blocking voltage.

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal resistance, junction to case	R _{ejc}	2.2	°C/W

ELECTRICAL CHARACTERISTICS ($T_c = 25^{\circ}C$ and either polarity of MT2 to MT1 voltage unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak off state current (Rated V_{DRM} @ T _c = 100°C, gate open)	I _{DRM}	-	-	2	mA
Peak on-state voltage (I _{TM} = 30A peak)	V _{TM}	-	1.7	2	Volts
DC gate trigger current (continuous dc) ($V_D = 12V$, $R_L = 12\Omega$) MT2(+), G(+) MT2(-), G(-)	I _{GT}	-	25 25	50 50	mA
DC gate trigger voltage (continuous dc) all polarities ($V_D = 12V$, $R_L = 100\Omega$) ($V_D = V_{DRM}$, $R_L = 125\Omega$, $T_C = 100$ °C)	V _{GT}	- 0.2	1.25 -	2.5	Volts
Holding current (either direction) ($V_D = 12V$, gate open, $I_T = 125$ mA)	I _H	-	20	60	mA
Gate controlled turn on time (V_D = Rated V_{DRM} , I_T = 10A, I_{GT} = 80mA, rise time = 0.1µs)	t _{gt}	-	1.6	-	μs



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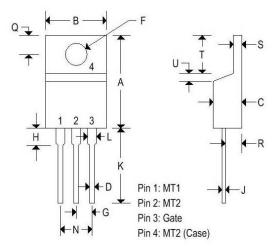
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Critical rate of rise of commutating voltage (Rated V _{DRM} , $I_{T(RMS)} = 8A$, commutating di/dt = 4.3A/ms, gate unenergized, T _c = 80°C)	dv/dt(c)	-	10	-	V/µs
Critical rate of rise of off-state voltage	dv/dt				V/µs
(Rated V_{DRM} , exponential voltage rise, gate open, T_{C} = 100°C)					
Т2802В		100	-	-	
T2802C		85	-	-	
T2802D		75	-	-	
T2802E		65	-	-	
T2802M		60	-	-	

MECHANICAL CHARACTERISTICS

Case	ТО-220АВ
Marking	Alpha-numeric
Pin out	See below



	TO-220AB			
	Inc	Inches		neters
	Min	Max	Min	Max
Α	0.575	0.620	14.600	15.750
В	0.380	0.405	9.650	10.290
С	0.160	0.190	4.060	4.820
D	0.025	0.035	0.640	0.890
F	0.142	0.147	3.610	3.730
G	0.095	0.105	2.410	2.670
Н	0.110	0.155	2.790	3.930
J	0.014	0.022	0.360	0.560
Κ	0.500	0.562	12.700	14.270
L	0.045	0.055	1.140	1.390
Ν	0.190	0.210	4.830	5.330
Q	0.100	0.120	2.540	3.040
R	0.080	0.110	2.040	2.790
S	0.045	0.055	1.140	1.390
T	0.235	0.255	5.970	6.480
U	1	0.050	197	1.270
۷	0.045	-	1.140	1
Ζ	1	0.080		2.030



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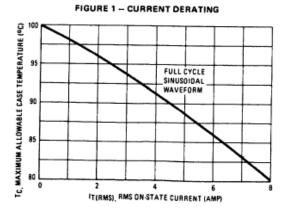


FIGURE 2 - POWER DISSIPATION 12 P(AV), AVERAGE POWER DISSIPATION (WATTS) 10 FULL CYCLE SINUSOIDAL WAVEFORM MAXIMUN TYPICAL 6 0 2 4 6 8 10 12 IT(RMS), RMS ON-STATE CURRENT (AMP)